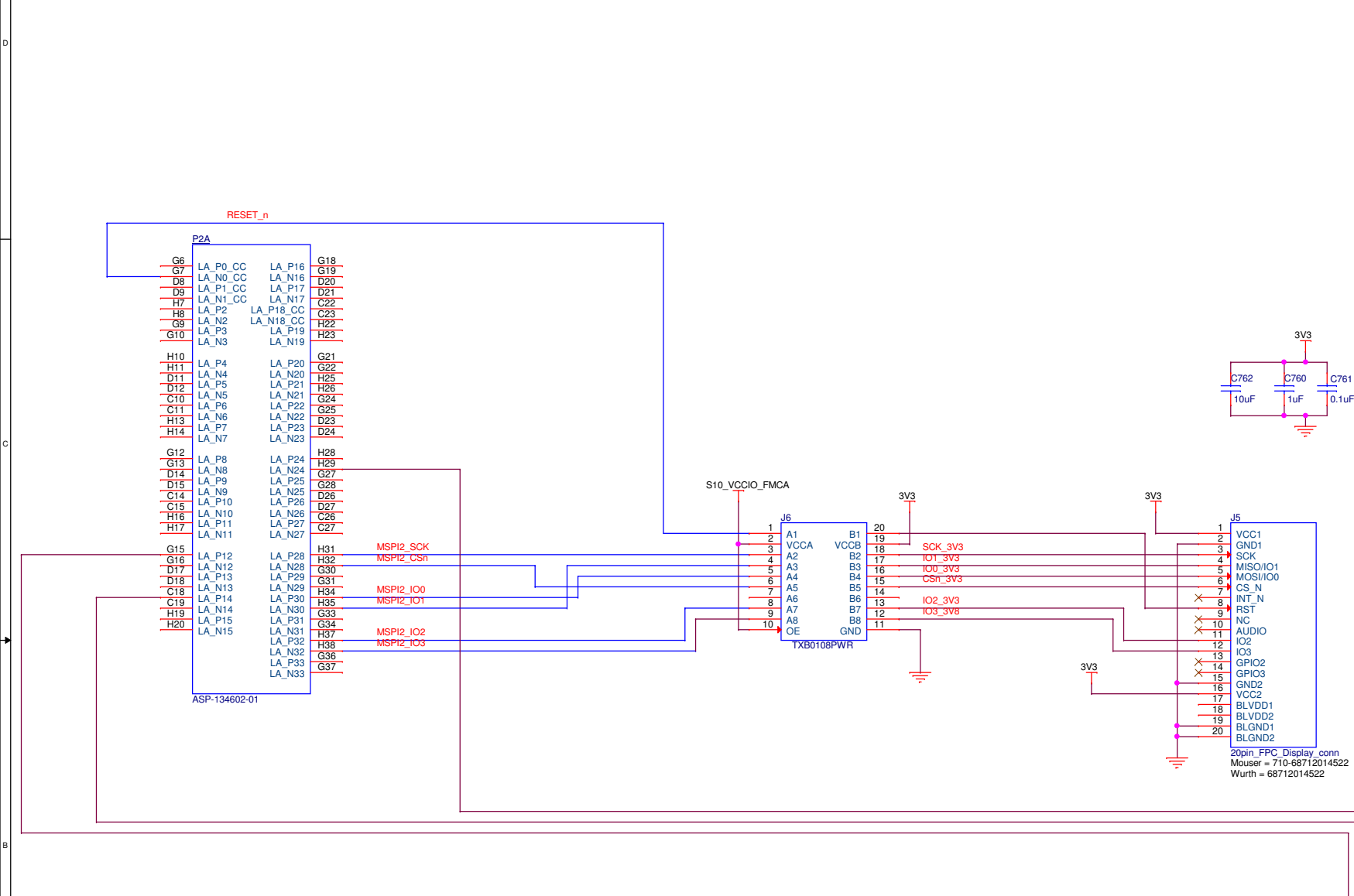


SPI x4 with FTDI controller Matrix Orbital Display



Adesto ATXP032 32Mb 266MB/s
MSPI0:

```
D0 = PIO 22 (MOSI)
D1 = PIO 26 (MISO)
D2 = PIO 4
D3 = PIO 23
D4 = PIO 0
D5 = PIO 1
D6 = PIO 2
D7 = PIO 3
SCK = PIO 24
CSn = PIO 15
```

APS6408L-OBx DDR Octal 64Mb SPI PSRAM
MSPI1:

```
SCK      = PIO 59
CSn      = PIO 50
RESETn   = RESETn_OUTPAD
DS       = PIO 60
DQ[7:0]  = pad[58:51]
```

APS6404L-SQH SPI/QPI PSRAM
MSPI2:

```
SCK           = PIO 68
CSn           = PIO 63
SI_SIO0       = PIO 64
SO_SIO1       = PIO 65
SIO2          = PIO 66
SIO3          = PIO 67
RESET         = RESETn_OUTPAD
```

DCX is on D[2] for all SPI x1 interfaces

